

Serial No. 09/648164

**IN THE UNITED STATES
PATENT AND TRADEMARK OFFICE****Patent Application****Inventor(s)** Yi-Feng Chyan
John Michael Hergenrother
Donald Paul Monroe**Case** 15-6-9**Serial No.** 09/648164**Group Art Unit** 2826**Filing Date** August 25, 2000**Examiner** T. L. Dickey**Title** Architecture For Circuit Connection Of A Vertical Transistor**ASSISTANT COMMISSIONER FOR PATENTS
WASHINGTON, D.C. 20231****SIR:****PETITION FOR EXTENSION OF TIME**

Applicant(s) petition(s) the Assistant Commissioner For Patents to extend the time for response to the Office action dated 11/29/01, for 2 month(s) from 12/29/01 to 03/01/02.

Please charge Lucent Technologies Inc. Deposit Account No. 12-2325 in the amount of \$390.00 to cover the cost of the extension. Any deficiency or overpayment should be charged or credited to Deposit Account No. 12-2325. Duplicate copies of this petition are enclosed.

Respectfully,

Ferdinand M. Romano, Attorney for Applicant(s)
Reg. No. 32752
407-371-3250Dated: 2/28/02

02/01/2002 JSIMONS 00/Agere Systems Inc.48164

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